

# Bibliography

- [ACV<sup>+</sup>00] RAJIV WICKREMESINGHE, LARS ARGE, JEFF CHASE, AND JEFFREY S. VITTER. Efficient Sorting Using Registers and Cache. ACM journal of Experimental Algorithmics, 2000.
- [AMD02] AMD Athlon Processor x86 Code Optimization Guide. World Wide Web document, [http://www.amd.com/us-en/assets/content\\_type/white\\_papers\\_and\\_tech\\_docs/22007.pdf](http://www.amd.com/us-en/assets/content_type/white_papers_and_tech_docs/22007.pdf), 2002.
- [AV88] ALOK AGGARWAL AND JEFFREY S. VITTER. The input/output complexity of sorting and related problems. Communications of the ACM, 1988.
- [BDFC00] MICHAEL A. BENDER, RICHARD COLE, ERIC DEMAINE, AND MARTIN FARACH-COLTON. Cache-oblivious b-trees. In Proceedings of the 10<sup>th</sup> Annual European Symposium on Algorithms, 2002.
- [BF02a] GERTH S. BRODAL AND ROLF FAGERBERG. Cache oblivious distribution sweeping. In Proceedings of the 29<sup>th</sup> International Colloquium on Automata, Languages and Programming, 2002.
- [BF02b] GERTH S. BRODAL AND ROLF FAGERBERG. Funnel Heap – A cache oblivious priority queue. In Proceedings of the 13<sup>th</sup> Annual International Symposium on Algorithms and Computation, 2002.
- [BFJ02] GERTH S. BRODAL, ROLF FAGERBERG, AND RICO JACOB. Cache Oblivious Search Trees via Binary Trees of Small Height, SODA, 2000.
- [BF03] GERTH S. BRODAL AND ROLF FAGERBERG. On the limits of cache-obliviousness. Proceedings of 35<sup>th</sup> Annual ACM Symposium on Theory of Computation. To appear, 2003.
- [BFP<sup>+</sup>73] MANUEL BLUM, ROBERT W. FLOYD, VAUGHAN PRATT, RONALD L. RIVEST, AND ROBERT E. TARJAN. Time bounds for selection. Journal of Computer and System Sciences, 1973.
- [C++98] INTERNATIONAL ORGANIZATION FOR STANDARDIZATION. ISO/IEC 14882: Standard for the C++ Programming Language, 1998.
- [CM99] ANDREAS CRAUSER AND KURT MEHLHORN. LEDA-SM A Platform for Secondary Memory Computation, Lecture Notes in Computer Science, 1999. See also <http://www.mpi-sb.mpg.de/~crauser/leda-sm.html>.
- [DB03] Datamation Benchmark. Sort Benchmark Home Page, hosted by Microsoft. World Wide Web document, <http://research.microsoft.com/barc/SortBenchmark/>, 2003.

- [Dem02] ERIK D. DEMAINE. Cache-oblivious Algorithms and Data Structures. Lecture notes, Massive Data Sets summer school, University of Aarhus, 2002.
- [EKZ77] PETER VAN EMDE BOAS, ROB KAAS, AND JAN ZIJLSTRA. Design and Implementation of an Efficient Priority Queue. *Mathematical Systems Theory* 10, 1977.
- [Flo72] ROBERT W. FLOYD. Permuting information in idealized two-level storage. *Complexity of Computer Calculations*, 1972.
- [FLPR99] MATREO FRIGO, CHARLES E. LEISERSON, HARALD PROKOP, AND SHIDHAR RAMACHANDRAN. Cache-oblivious algorithms. *Proceedings of the 40<sup>th</sup> Annual Symposium on Foundations of Computer Science*, New York, 1999.
- [HSU<sup>+</sup>01] GLENN HINTON, DAVE SAGER, MIKE UPTON, DARRELL BOGGS, DOUG CARMEAN, ALAN KYKER, AND PATRICE ROUSSEL. The Microarchitecture of the Pentium® 4 processor. World Wide Web document, [http://www.intel.com/technology/itj/q12001/articles/art\\_2.htm](http://www.intel.com/technology/itj/q12001/articles/art_2.htm), 2001.
- [HK81] JIA.WEI HONG AND H. T. KUNG. I/O complexity: The red-blue pebble game. *Proceedings of the 13<sup>th</sup> Annual ACM Symposium on Theory of Computation*, Milwaukee, Wisconsin, 1981.
- [HP96] JOHN L. HENNESSY AND DAVID A. PATTERSON. *Computer Architecture: A Quatitative Approach*, second edition. Morgan Kaufmann, 1996.
- [Joh01] DAVID S. JOHNSON. A Theoretician’s Guide to the Experimental Analysis of Algorithms. *Proceedings of the 5<sup>th</sup> and 6<sup>th</sup> DIMACS Implementation Challenges*. Goldwasser, Johnson, and McGeoch (eds), American Mathematical Society, 2001.
- [Knu98] DONALD E. KNUTH. *The Art of Computer Programming, Vol. 3: Sorting and Searching*, second edition, Addison-Wesley, 1998.
- [LFN02] RICHARD E. LADNER, RAY FORTNA, AND BAO-HOANG NGUYEN. A Comparison of Cache Aware and Cache Oblivious Static Search Trees Using Program Instrumentation, 2002.
- [LL99] ANTHONY LAMARCA AND RICHARD E. LADNER. The Influence of Caches on the Performance of Sorting, *Journal of Algorithms* Vol. 31, 1999.
- [MPS02] MIPS64™ Architecture for Programmers, Vol. 1: Introduction to the MIPS64™ Architecture. World Wide Web document, <http://www.mips.com/content/Documentation/MIPSDocumentation/Process orArchitecture/doclibrary>, 2002.
- [MR01] CONRADO MARTÍNEZ AND SALVADOR ROURA. Optimal Sampling Strategies in Quicksort and Quickselect. *SIAM Journal of Computing*, 2001.
- [Mus97] DAVID R. MUSSER. Introspective Sorting and Selection Algorithms. *Software – Practice and Experience*, vol. 9, nr. 8, 1997.

- [NM95] STEFAN NÄHER AND KURT MEHLHORN. The LEDA Platform of Combinatorial and Geometric Computing, Communications of the ACM, 1995.
- [Neu98] KARL-DIETRICH NEUBERT. The Flashsort1 algorithm. Dr. Dobb's journal, 123-125, 1998.
- [OS02] JESPER H. OLSEN AND SØREN C. SKOV. Cache-Oblivious Algorithms in Practice, 2002.
- [PAPI03] Performance application programming interface. World Wide Web document, <http://icl.cs.utk.edu/projects/papi/>, 2003.
- [RR00] NAILA RAHMAN AND RAJEEV RAMAN. Analysing cache effects in distribution sorting, ACM journal of Experimental Algorithms, Vol. 5, 2000.
- [Sav98] JOHN E. SAVAGE. Models of Computation. Addison Wesley Longman, Inc., 1998.
- [Sed78] ROBERT SEDGEWICK. Implementing Quicksort Programs. Communications of the ACM, 21(10), 1978.
- [ST85] DANIEL D. SLEATOR AND ROBERT E. TARJAN. Amortized efficiency of list update and paging rules. Communications of the ACM, 1985.
- [Tan99] ANDREW S. TANENBAUM. Structured Computer Organization, 4<sup>th</sup> edition. Prentice-Hall Inc., 1999.
- [Tan01] ANDREW S. TANENBAUM. Modern operating systems, 2<sup>nd</sup> edition. Prentice-Hall Inc., 2001.
- [TPI02] Duke University. A Transparent Parallel I/O Environment, World Wide Web Document, <http://www.cs.duke.edu/TPIE/>, 2002.
- [Vit01] JEFFREY S. VITTER. External Memory Algorithms and Data Structures: Dealing with Massive Data. ACM Computing Surveys, 2001.
- [Wil64] JOHN W. J. WILLIAMS. Algorithm 232: Heapsort. Communications of the ACM, 1964.
- [XZK00] LI XIAO, XIAODONG ZHANG, AND STEFAN A. KUBRICHT. Improving Memory Performance of Sorting Algorithms, ACM Journal of Experimental Algorithms, Vol. 5, 2000.
- [Yea96] KENNETH C. YEAGER. The MIPS R10000 Superscalar Microprocessor. IEEE Micro, 16(2), 28-40, 1996.